Amendments to the Claims:

This listing of the claims will replace all prior versions, and listings, of the claims in the application:

1. (Currently Amended) An electrical component, comprising:

a capacitor having a first end and a second end; and

a circuit coupled to the capacitor, the circuit including discrete magnetically-coupled windings such that the magnetic induction of the discrete magnetically-coupled windings provides capacitor-path inductance cancellation,

wherein induction of the mutually coupled windings generates a voltage that counteracts a voltage due to equivalent series inductance of the capacitor and not a voltage due to the capacitance of the capacitor.

2. (Original) The component according to claim 1, wherein the coupled windings are discrete windings.

3. (Original) The component according to claim 1, wherein the coupled windings are integrated with the capacitor.

4. (Original) The component according to claim 1, wherein the coupled windings are wound on a former.

5. (Original) The component according to claim 4, wherein the former is substantially non-magnetic.

6. (Original) The component according to claim 1, wherein the coupled windings are formed from foil.

7. (Original) The component according to claim 1, wherein the coupled windings are formed on a flexible material.

8. (Original) The component according to claim 1, wherein the coupled windings are formed on a printed circuit board.

9. (Original) The component according to claim 1, wherein the coupled windings include a structure having an air core.

10. (Original) The component according to claim 1, wherein the coupled windings include a magnetic material.

11. (Canceled)

12. (Canceled)

13. (Canceled)

14. (Original) The component according to claim 1, wherein the component has three terminals.

15. (Original) The component according to claim 1, wherein the coupled windings include first and second coils and a first terminal coupled to a first end of the first coil and a first end of the second coil, a second terminal coupled to a second end of the second coil, and wherein the second end of the capacitor is coupled to a second end of the first coil.

16. (Original) The component according to claim 15, wherein a third terminal is coupled to the first end of the capacitor.

17. (Original) The component according to claim 1, wherein the coupled windings include first and second coils and a first terminal coupled to a first end of the first coil, a second terminal connected to the second end of a second coil, and wherein the second end of the capacitor is coupled to a second end of the first coil and to the first end of the second coil.

18. (Original) The component according to claim 17, wherein the first and second coils are constructed as a single coil with a tap.

19. (Original) The component according to claim 17, wherein a third terminal is coupled to the first end of the capacitor.

20. (Original) The component according to claim 1 wherein the coupled windings are wound about a package containing the capacitor.

21. (Original) The component according to claim 1, wherein the coupled windings generate a negative equivalent inductance in series with the capacitor.

22. (Canceled)

23. (Original) The component according to claim 1, wherein the coupled windings are formed from a single tapped winding.

24. (Original) The component according to claim 1, wherein the coupled windings have a mutual inductance greater than one of the self inductances.

25. (Original) The component according to claim 24, wherein the mutual inductance of the coupled windings minus the self inductance of one of the coupled windings is substantially equal to the equivalent series inductance of the capacitor plus any interconnect inductance.

26. (Original) The component according to claim 1, wherein the coupled windings have a mutual inductance that is substantially of the same magnitude as the equivalent series inductance of the capacitor plus any interconnect inductance.

27. (Currently Amended) A method of suppressing electrical signals, comprising:

coupling a circuit including discrete magnetically coupled windings to a capacitor having first and second ends; and

selecting a mutual inductance of the coupled windings to nullify an inductance of the capacitor electrical path,

wherein the capacitance of the capacitor is not nullified.

28. (Original) The method according to claim 27, further including modeling the winding circuit with a T model having a first leg, a second leg and a third leg, wherein the third leg is coupled to the capacitor.

29. (Original) The method according to claim 28, further including providing the third leg with a negative inductance.

30. (Original) The method according to claim 29, further including modeling the capacitor as having a capacitance and an equivalent series inductance, which is canceled by the negative inductance of the third leg of the T model.

31. (Original) The method according to claim 27, further including selection of a connection point of the coupled winding circuit by finding the point that minimizes the magnitude of the output signal when an input signal is applied.

32. (Original) The method according to claim 27, further including forming discrete windings.

33. (Original) The method according to claim 27, further including integrating the capacitor and the winding circuit.

34. (Canceled)

35. (Original) The method according to claim 27, further including setting the mutual inductance of the coupled windings larger than the self inductance of one of the winding.

36. (Original) The method according to claim 35, further including setting the difference between a mutual inductance of the coupled windings and the self inductance of one of the windings substantially equal to the equivalent series inductance of the capacitor electrical path.

37. (Original) The method according to claim 27, further including setting the magnitude of a mutual inductance of the coupled windings substantially equal to the equivalent series inductance of the capacitor electrical path.

38. (Currently Amended) A filter, comprising:

a capacitive element; and

a circuit coupled to the capacitive element, the circuit including discrete magnetically coupled windings for nullifying the effect of an equivalent series inductance of a path through the capacitive element, wherein the effect of the capacitance of the capacitor is not nullified.

39. (Original) The filter according to claim 38, wherein the coupled windings are discrete windings.

40. (Original) The filter according to claim 38, wherein the coupled windings are integrated with the capacitive element.

41. (Original) The filter according to claim 38, wherein the coupled windings are formed on a flexible material.

42. (Original) The filter according to claim 38, wherein the coupled windings include a structure having an air core.

43. (Original) The filter according to claim 38, wherein the coupled windings include a magnetic material.

44. (Canceled)

45. (Original) The filter according to claim 38, wherein the filter has three terminals.

46. (Original) The filter according to claim 38, wherein the coupled windings are wound about a package containing the capacitive element.

47. (Original) The filter according to claim 38 wherein the magnitude of the mutual inductance of the coupled windings is substantially equal to the equivalent series inductance of the capacitive element plus any interconnect inductance.

48. (Original) The filter according to claim 38 wherein the mutual inductance of the coupled windings is larger than the self inductance of one of the windings.

49. (Original) The filter according to claim 48 wherein the difference between the mutual inductance of the coupled windings and the self inductance of one of the windings is substantially equal to the equivalent series inductance of the capacitive element plus any interconnect inductance.

50-78. (Canceled)